



Sri Satya Sai University of Technology & Medical Sciences, Sehore (M.P)
Scheme of Examination - CBCS Pattern
Academic Year 2017- 2018

Branch :Electrical Engineering

Semester - III

S.No.	Subject Code	Subject Name	Maximum Marks Theory Slot			Maximum Marks (Practical Slot)		Periods/ hour/ week			Credits	Total Marks
			End Sem. Exam.	Mid Tests	Assign-ments/Quiz	End Sem. Practical & Viva	Practical Record /Assignment/ Quiz / Presentation	L	T	P		
1	MTH - 301	Computational Techniques	60	30	10	-	-	2	1	-	3	100
2	EEC- 302	Electronics Devices & Circuits	60	30	10	30	20	2	1	2	4	150
3	EEC- 303	Digital Circuits	60	30	10	30	20	2	1	2	4	150
4	EEC- 304	Network Analysis and Synthesis	60	30	10	30	20	2	1	2	4	150
5	EEC- 305	Signals & Systems	60	30	10	30	20	2	1	2	4	150
6	EEC- 306	Instrumentation and Measurement	60	30	10	30	20	2	1	2	4	150
TOTAL			360	180	60	150	100	12	6	10	23	850

w.e.f July 2017