



# Sri Satya Sai University of Technology & Medical Sciences, Sehore (M.P)

## Scheme of Examination - CBCS Pattern

Academic Year 2017- 2018

Branch :Electrical and Electronics Engineering

Semester - III

S.No.	Subject Code	Subject Name	Maximum Marks Theory Slot			Maximum Marks (Practical Slot)		Periods/ hour/ week			Credits	Total Marks
			End Sem. Exam.	Mid Tests	Assign-ments/Quiz	End Sem. Practical & Viva	Practical Record /Assignment/ Quiz / Presentation	L	T	P		
1	EXC- 301	Computational Techniques	60	30	10	-	-	2	1	-	3	100
2	EXC- 302	Electronics Devices & Circuits	60	30	10	30	20	2	1	2	4	150
3	EXC- 303	Digital Circuits	60	30	10	30	20	2	1	2	4	150
4	EXC- 304	Network Analysis and Synthesis	60	30	10	30	20	2	1	2	4	150
5	EXC- 305	Signals & Systems	60	30	10	30	20	2	1	2	4	150
6	EXC- 306	Instrumentation and Control	60	30	10	30	20	2	1	2	4	150
<b>TOTAL</b>			<b>360</b>	<b>180</b>	<b>60</b>	<b>150</b>	<b>100</b>	<b>12</b>	<b>6</b>	<b>10</b>	<b>23</b>	<b>850</b>

w.e.f July 2017